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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jason M. Howard et al.

Title: MULTI-THREADED MULTIPLY ACCUMULATOR

Docket No.: 884.584US1

Filed: February 8, 2002

Examiner: Chat C Do

Serial No.: 10/071,373

Due Date: January 11, 2007

Group Art Unit: 2193

**MS Appeal Brief - Patents**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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☒ Response to Notice of Non-Compliant Appeal Brief (3 pgs.).

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Robert B. Madden

Atty: Robert B. Madden

Reg. No. 57,521

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Name Amy Moriarty

Signature Amy Moriarty

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



S/N 10/071,373

PATENT

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Serial No.: 10/071,373

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Title: MULTI-THREADED MULTIPLY ACCUMULATOR

Assignee: Intel Corporation

Customer Number: 21186

**RESPONSE TO NOTICE OF NON-COMPLIANT APPEAL BRIEF**

MS Appeal Brief - Patents  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This responds to the Notice of Non-Compliant Appeal Brief mailed on December 11, 2006. In compliance with MPEP 1205.03(B) and 37 CFR 41.37(c)(1)(v), Appellants submit the following corrected section from Appellants' previously-submitted Appeal Brief filed May 24, 2006.

Please replace the previously-submitted Summary of Claimed Subject Matter Section 5 with the below replacement:

## **5. SUMMARY OF CLAIMED SUBJECT MATTER**

### **Claim 8**

Some aspects of the present inventive subject matter include, but are not limited to an integrated circuit such as integrated circuit 200 as illustrated in FIG. 2 comprising a multiplier such as 232 coupled to receive interleaved operands and to produce a product; and a multi-threaded accumulator such as multi-threaded accumulator circuit 100 coupled to the multiplier to receive the product. *Page 5, line 18 through page 6, line 12.*

### **Claim 16**

Some aspects of the present inventive subject matter include, but are not limited to an accumulator circuit such as circuit 100 as illustrated in FIG. 2, to accept operands from different threads interleaved in time, the accumulator having intermediate registers to simultaneously hold partial results from each of the different threads. *Page 3, line 2 through page 6, line 12.*

### **Claim 23**

Some aspects of the present inventive subject matter include, but are not limited to a multi-threaded floating point multiply-accumulator circuit such as multi-threaded floating point multiply accumulate circuit 300 as illustrated in FIG. 3, comprising a multiplier to produce a product; and an accumulator coupled to receive the product from the multiplier, the accumulator including sequential elements to provide a multi-threaded capability. *Page 3, line 20-29; page 6, line 27 through page 7, line 4; page 15, lines 6-9.*

This summary does not provide an exhaustive or exclusive view of the present subject matter, and Appellants refer to the appended claims and its legal equivalents for a complete statement of the invention.

**Conclusion**

In accordance with MPEP 1205.03(B) and 37 CFR 41.37(c)(1)(v), only the non-compliant section of Appellants' previously-submitted Appeal Brief has been included in this response.

Appellants respectfully request that the Examiner withdraw the non-compliant status and examine the Appeal Brief.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

KENNETH S. CHEW

By their Representatives,  
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Attorneys for Intel Corporation  
P.O. Box 2938  
Minneapolis, Minnesota 55402  
(612) 373-6904

Date JANUARY 9/2007 By Robert B. Madden  
Robert B. Madden  
Reg. No. 57,521

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Amy Moriarty  
Name

[Signature]  
Signature